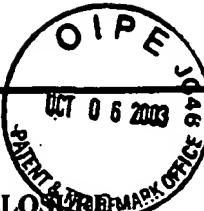


FORM PTO/SB/08A/B (10-01)  
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Attorney Docket Number	50472/RJP/B600
Application Number	09/892,242
Filing Date	June 26, 2001
Applicant(s)	Zheng Qi, et al.
Group Art Unit	2131
Examiner Name	not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>3</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>3</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)
PP		0 661 843 A2 EPX	07-05-1995	Alvarez	RECEIVED
PP		EP 0 661 843 A3	11-10-1999	Alvarez	OCT 08 2003
					Technology Center 2100

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
PP		European Search Report dated September 1, 2003 from corresponding European Patent Application No. 01309324.0-1525, filed November 2, 2001
PP		JENKINS, et al.; <i>I.C. multiplexer increases analogue switching speeds</i> ; Electronic Engineering; Vol. 45, No. 540; February 1973; pp. 73-75; XP-000807191
PP		MCLLOONE et al.; <i>A High Performance FPGA Implementation of DES</i> ; 2000 IEEE Workshop On Signal Processing Systems, SIPS 2000. Design and Implementation (CAT. NO. 00TH8528), October 2000; pp. 374-382 XP-002251022

DAA PAS529355.1 - 10/2/03 10:52 AM

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